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METHOD FOR AUTOMATICALLY AND SAFELY RECOVERING BIOS MEMORY CIRCUIT IN MEMORY DEVICE INCLUDING DOUBLE BIOS MEMORY CIRCUITS

ABSTRACT OF THE DISCLOSURE

A method for recovering a basic input/output system (BIOS) memory circuit in a computer system is disclosed. The method includes steps of providing a memory device comprising a first basic input/output system memory circuit and a second basic input/output system memory circuit, the first basic input/output system memory circuit and the second basic input/output system memory circuit respectively having a first computer program and a second computer program stored therein, wherein the first basic input/output system memory circuit and the second basic input/output system memory circuit are employed to initiate an operation of the computer system, enabling the second basic input/output system memory circuit upon booting the computer system, detecting if the first computer program includes an error, and re-programming the first basic input/output system memory circuit based on the second computer program when the error is detected in the first computer program.